

		ISSUE CLASSIFICATION
Class	Subclass	

PATENT NUMBER

U.S. UTILITY Patent Application

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APPLICATION NO. 09/871192	CONT/PRIOR	CLASS 303 439	SUBCLASS -78	ART. UNIT 3613 2839	EXAMINER LE, T.
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TITLE	APPLICANTS
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Mark Haller

High integration electronic assembly and method

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="border-bottom: 1px solid black; margin-bottom: 10px;"> _____ (Assistant Examiner) _____ (Date) </div> <div style="border-bottom: 1px solid black; margin-bottom: 10px;"> _____ (Primary Examiner) _____ (Date) </div> <div style="border-bottom: 1px solid black;"> _____ (Legal Instruments Examiner) _____ (Date) </div>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <div style="border-bottom: 1px solid black; margin-top: 10px;"></div> <div style="border-bottom: 1px solid black; margin-top: 10px;"></div>				ISSUE FEE	
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